

Cl Select bl, /bl(/UB) Select bl, bl(/LB) 208U | cl z(/UB,/LB) Cl Control Mask Input dmx(/UB,/LB). Data Input cdbz(/UB) Data Input cdbz(/LB) bwrpz **2070** 205 Timing Delay 207L pwdlupz,pwdllpz wdlupz,wdllp<u>z</u> dmlpz wrpx 202 Command Mask Input Control Data Input Control 203 DQ(/UB)-KE DQ(/LB)

F I G. 2

FIG. 3A

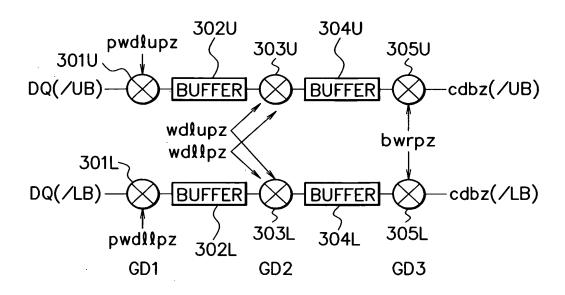
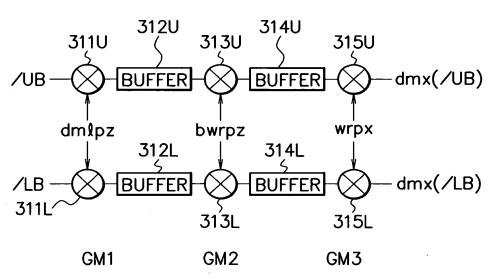
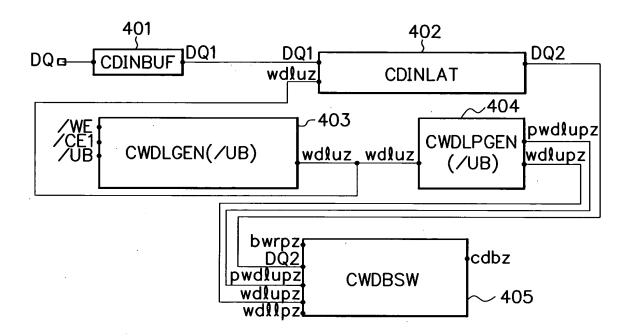


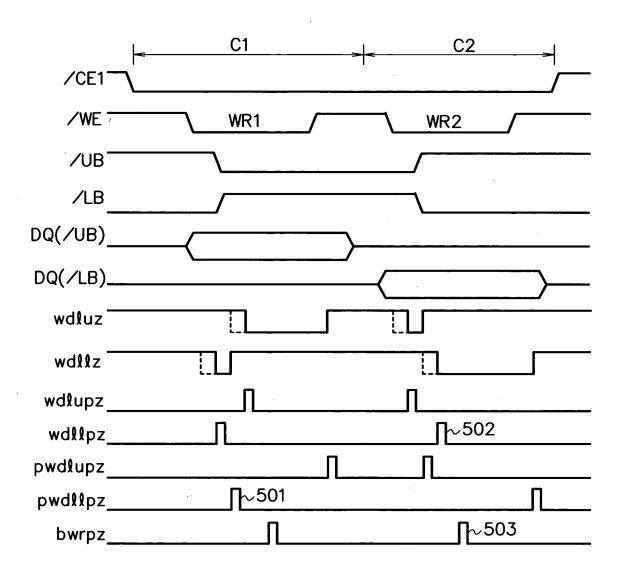
FIG. 3B



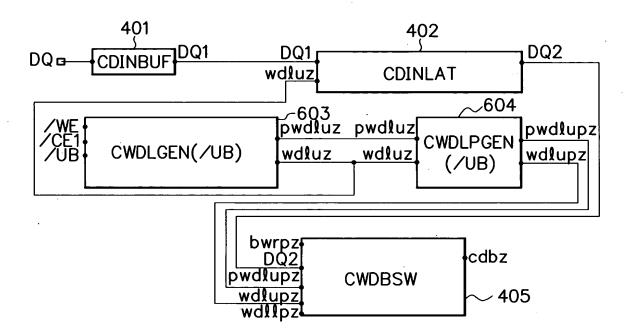
F I G. 4



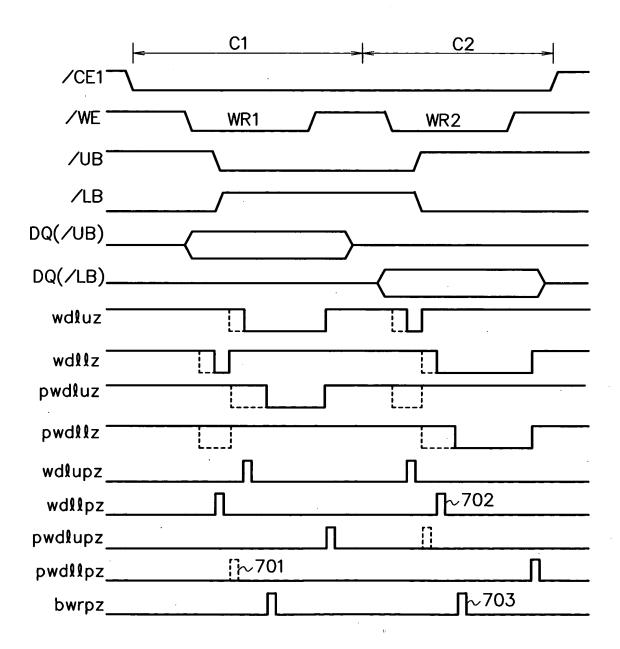
F I G. 5

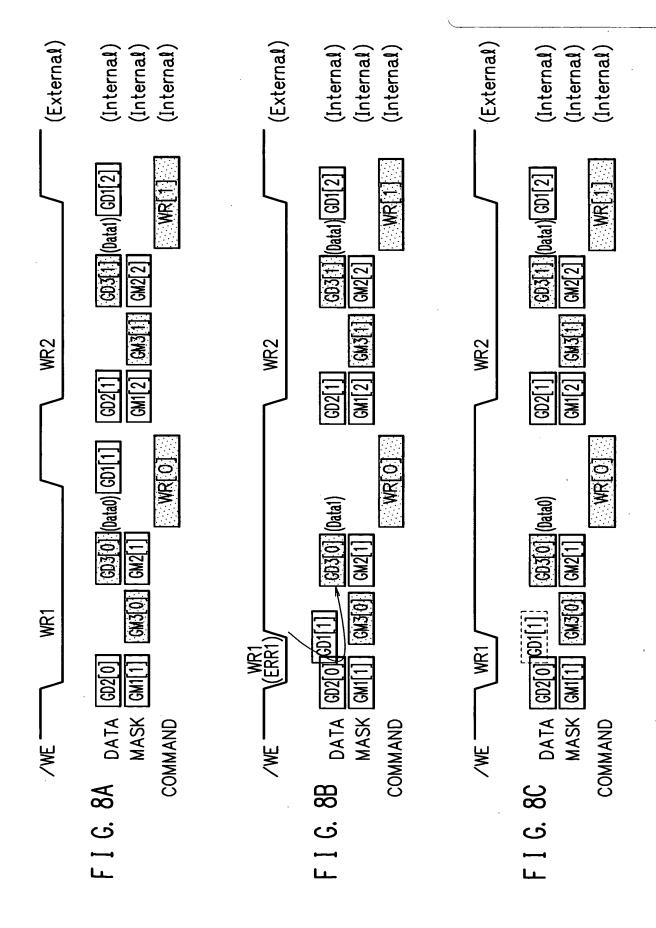


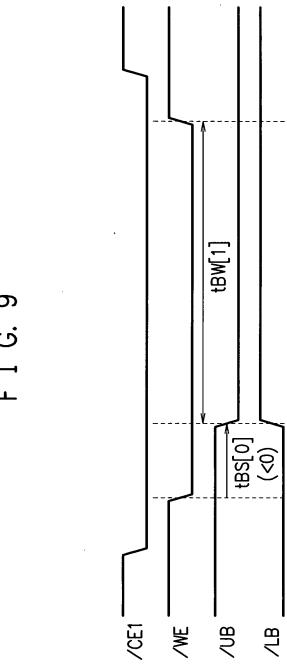
F I G. 6



F I G. 7



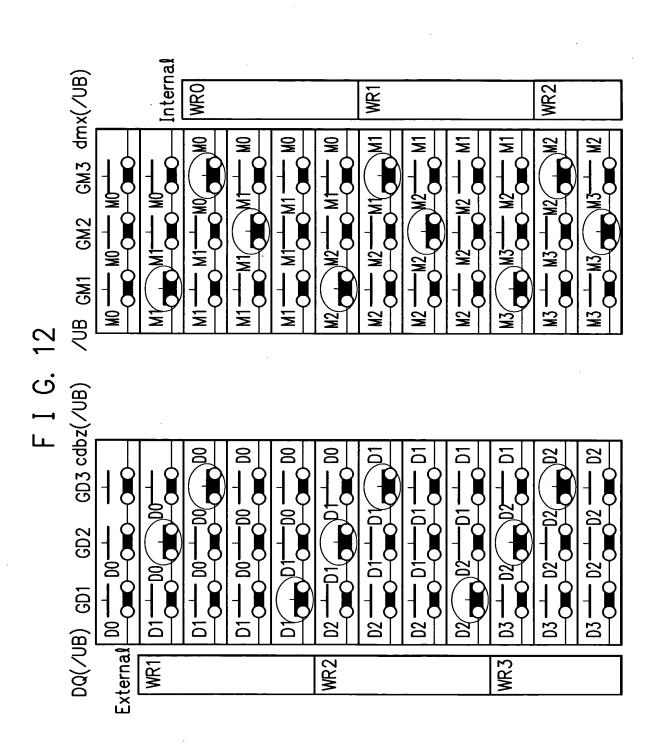


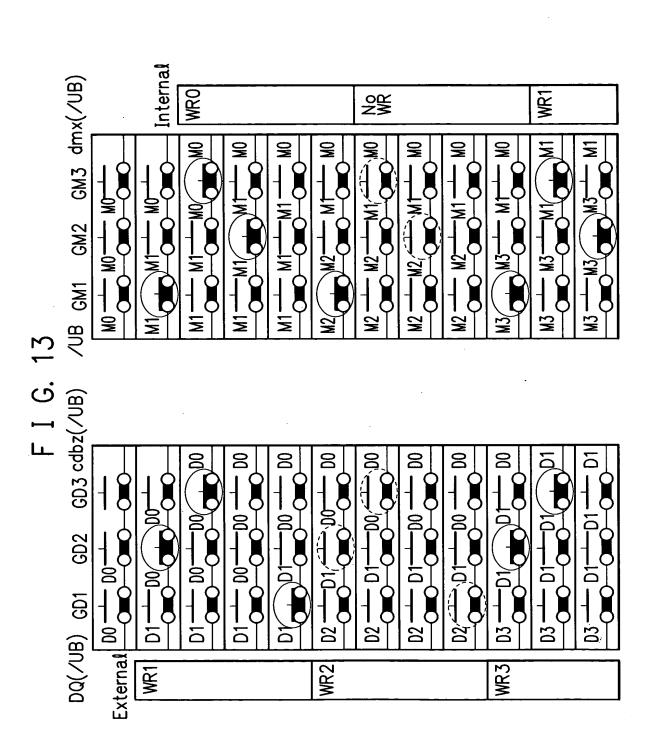


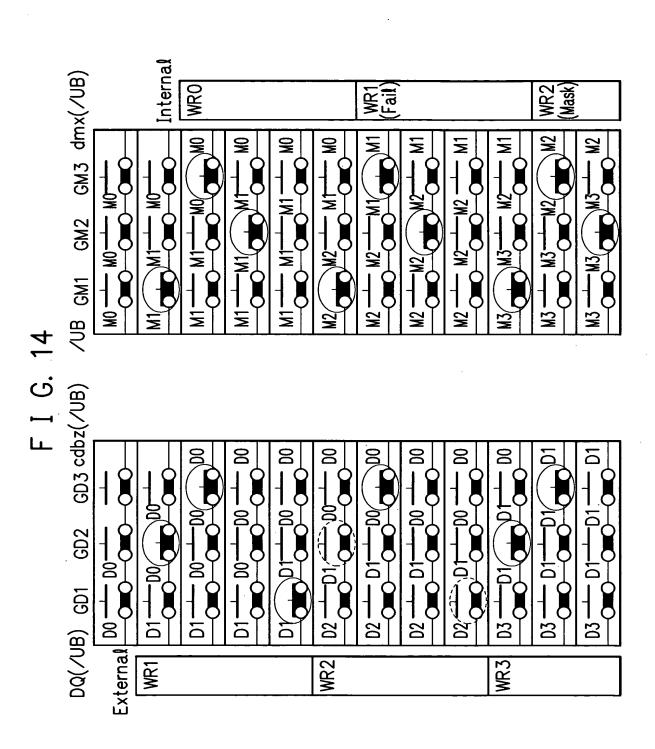
RECEIVE							MASK						
	OPERATIONAL MODE					0 tBS(ns)		OPERATIONAL MODE					0 tBS(ns)
NO WR	-	WR OPERATION	/UB =" H" RECEIVE	DATA TRANSFER	DATA RECEIVE		NO WR		WR OPERATION	/UB =" H" RECEIVE	DATA TRANSFER	DATA RECEIVE	
DATA CORRUPTION	4 3a 2a		UNDEFINED				DATA CORRUPTION	4 3b 2b		ONDETINED			
W.	5	WR [UB] EXECUTION	/UB ="L" RECEIVE UN	TRANSFER EXECUTION	RECEIVE EXECUTION		WR.	5	WR [UB] EXECUTION	/UB ="L" RECEIVE UN	TRANSFER EXECUTION	RECEIVE EXECUTION	
F I G. 10A	J	CD3	GM1	GD2 [001	I	F I G. 10B		CD3 [GM1	GD2 [00	ı
F I G							F I G						

WR2 WR1 **D3** WR3 **M**3 (WR2) **D**2 M2WR1 No WR WR0 WR0 WR1 Ξ 딥 . 108 7B o O O WE.

F 1 G 1







GM3 dmx(/UB) WRO Š₹ Fail GM2 /UB GM1 GD3 cdbz(/UB) DQ(/UB) GD1 GD2 Externaℓ WR2 WR3 WR1

F I G. 16

			// // //	RECEIVE						
	OPERATIONAL MODE								A	0 tBS(ns)
NO WR	1	WR OPERATION		/UB = "H" RECEIVE	DATA TRANSFER			DATA RECEIVE)	
Z	2c			∕JB						
	3 2d 2c									
	3		1							-
DATA CORRUPTION	4			UNDEFINED						-
W.	5	WR [UB] EXECUTION		/UB ="L" RECEIVE		TRANSFER EXECUTION		RECEIVE EXECUTION		
	<u> </u>	GD3	. , ,	GM1		GD2		GD1	•	I

